

Northwest Logic Announces the Immediate Availability of its Espresso Solution For x8 lane PCI Express® 2.0 For Xilinx Virtex-5 FXT FPGA Platform

The Espresso 2.0 Solution is hardware proven and PCI-SIG® Certified

Beaverton, Oregon, November 20, 2008 -- Northwest Logic announces the immediate availability of a high-performance, hardware-proven, x8 lane Espresso 2.0 Solution for PCI Express® 2.0 for Xilinx's Virtex®-5 FXT FPGA platform. This solution combines Northwest Logic's full-featured x8 Espresso 2.0 Core for PCI Express 2.0 and software to provide a complete, pre-packaged x8 lane PCI Express 2.0 solution. The solution enables high-performance x8 PCI Express 2.0 designs to be quickly developed for Virtex-5 FXT FPGAs.

Northwest Logic's x8 Espresso 2.0 Core and DMA Back-End Core supports high-performance, on-demand, multi-DMA engine operation with 3,000 Mbytes/s Card-to-System and 2,600 Mbytes/s System-to-Card throughput on a Intel DX58SO platform. Northwest Logic's companion DMA Driver (Window or Linux) manages the DMA engine and associated descriptors. For a complete system design, Northwest Logic optionally provides its high-performance DDR3/DDR2/DDR SDRAM Controller Cores. For more information, contact Northwest Logic at info@nwlogic.com or visit www.nwlogic.com.

"Virtex-5 FXT FPGAs offer the first programmable logic platform to support 5Gbps x8 lane operations for PCI Express 2.0," said Alex Goldhammer, Solution Marketing Manager for PCI Express Architecture Implementations at Xilinx. "The combination of Xilinx's ISE Design Suite, Virtex-5 FXT FPGAs and Northwest Logic's x8 Espresso 2.0 Solution creates a robust development platform for PCI Express 2.0 designers." For more information, please visit www.xilinx.com/pcie

Northwest Logic's x8 Espresso 2.0 Solution is PCI-SIG certified and hardware validated using HiTech Global's HTG-V5 board for PCI Express. "We are pleased to be

Northwest Logic Announces The Availability Of a Complete PCI Express 2.0 Solution

the first provider of a hardware-proven x8 lane endpoint board for PCI Express 2.0 using Xilinx's Virtex-5 FXT FPGA platform and Northwest Logic's x8 Expresso 2.0 Solution," said Cyrus Merati, HiTech Global's Vice President. This board and associated support package is available for immediate purchase at:

<http://www.HiTechglobal.com/Boards/V5PCIExpress.htm>.

"We are pleased to provide the first x8 lane design platform for PCI Express 2.0 in the FPGA marketplace. Our partnership with Xilinx and HiTech Global has enabled Northwest Logic to release a robust solution that is fully PCI-SIG certified and hardware-validated. This solution provides a high-performance, easy-to-use platform for developing x8 PCI Express 2.0 designs." said Brian Daellenbach, Northwest Logic's President.

About Northwest Logic

Northwest Logic located in Beaverton, Oregon, provides high-performance, easy-to-use IP cores for FPGAs and ASICs. For more information, visit www.nwlogic.com.

About HiTech Global

HiTech Global, located in San Jose, California, provides a broad array of boards and IP Cores. For more information, visit www.HiTechglobal.com