



MoSys and Northwest Logic Offer Integrated PCI Express and DDR3 Solutions

SUNNYVALE, Calif., July 14, 2010 — [MoSys, Inc. \[NASDAQ: MOSY\]](#), a leading provider of differentiated, high-density memory and high-speed interface (I/O) intellectual property (IP), and [Northwest Logic](#), a leading semiconductor IP provider, offer integrated PCI Express 2.0® and DDR3 solutions.

The PCI Express 2.0 solution combines MoSys' PHY for PCI Express 2.0 and Northwest Logic's full-featured Espresso 2.0 Core, DMA Back-End Core, DMA Driver and Espresso GUI to provide a complete, pre-packaged PCI Express 2.0 solution. The DDR3 solution combines MoSys' DDR3 PHY and Northwest Logic's high-performance DDR3 SDRAM Controller Core and add-on cores (AXI/AHB, Multi-Port, Reorder, etc.). These solutions are available separately or as a single, fully integrated solution.

"Northwest Logic's high performance, easy-to-use controller solutions makes them a strong partner for MoSys. Our partnership demonstrates our commitment to providing customers with a complete, integrated PCI Express 2.0 and DDR3 solutions," said David DeMaria, Vice President of Business Operations at MoSys. "Our customers receive a proven, validated and comprehensive solution that incorporates best-in-class technology that only expert IP providers like MoSys and Northwest Logic can offer."

"This combined controller + PHY solution is optimized for high-performance across a broad range of process technologies. In addition, MoSys and Northwest Logic provide both customization and integration services to ensure customers are delivered a solution which meets their exact requirements," said Brian Daellenbach, President of Northwest Logic.

These solutions, along with high quality documentation, comprehensive verification suites and expert technical support enable SoC designers to quickly get their products to market with minimal cost and risk. Further details for the integrated PCI Express 2.0/ DDR3 solutions is available from both companies. Contact MoSys at www.mosys.com/contact.php and Northwest Logic at <http://www.nwlogic.com/inforequest.html> .

About MoSys, Inc.

Founded in 1991, MoSys® (NASDAQ: MOSY), develops, markets and licenses differentiated embedded memory and high speed parallel and serial interface IP for advanced SoC designs. MoSys' patented 1T-SRAM® and 1T-Flash® memory technologies offer a combination of high density, low power consumption, high speed and low cost advantages that are unmatched by other available memory technologies for a variety of networking, computing, storage and consumer/graphics applications. MoSys' silicon-proven interface IP portfolio includes DDR3 PHYs, as well as SerDes IP that support data rates from 1 Gigabit per second (Gbps) to 11 Gbps, across a wide range of standards, including PCI Express, XAUI, SATA and 10G KR. MoSys IP has been production-proven in more than 225 million devices. MoSys is headquartered in Sunnyvale, California. More information is available on MoSys' website at www.mosys.com.

About Northwest Logic

Northwest Logic, founded in 1995 and located in Beaverton, Oregon, provides high performance, easy-to-use IP cores for ASICs and FPGAs. These IP cores include Memory Controller, PCI Express, PCI-X, PCI and MIPI cores.

Key benefits of Northwest Logic's IP cores include:

- High performance – support high clock rate and high throughput
- Easy to use – simple user interface, easy to configure, etc.
- Fully silicon validated
- Provided with a comprehensive verification suite
- Support for MoSys' PHYs
- Top quality technical support
- Customization and integration services available

For more information please contact info@nwlogic.com or visit www.nwlogic.com

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Media Contact

Katie Olivier

972-239-5119 x128

kolivier@sheltongroup.com

Company Contact

Kristine Perham

408-731-1804

kperham@mosys.com